

REMARKS

Claims 2-12 are pending in the application.

Claims 2, 4, and 7-12 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Takagi et al. (U.S. 6,188,682) in view of Sawahashi et al. (5,768,306).

Claims 3, 5 and 6 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Takagi et al., Sawahashi et al., and Shou et al. (U.S. 5,910,948).

Regarding the reasons for rejection

The Examiner has newly cited Takagi for the previous amendments. Takagi appears to have been cited to indicate that it is publicly known to sequentially generate candidates for a despread code and to select a despread code to be used from among the codes.

The present invention comprises: received signals stored in memory being repeatedly read, a single code being sequentially generated from a plurality of candidate codes, and one despread code and its timing being selected from among the plurality of candidate codes.

In this case, the present invention has the following advantages.

(1) The same timing environment obtained when the received signals are stored in memory is maintained.

(2) Timing mismatching due to a clock deviation can be solved.

(3) Furthermore, high-speed processing can reduce process time.

Sawahashi et al. discloses a technology for reading signals stored in memory and detecting the timing of a specific code.

Takagi et al. is provided with a plurality of fingers and discloses a technology for reducing a circuit scale by freely assigning the fingers to cell search, control channel signal reception and communication channel reception appropriately depending on a state.

However, neither Sawahashi et al. nor Takagi et al. disclose, teach, or suggest repeatedly reading received signals stored in memory, and sequentially generating a single code from a plurality of candidate codes.


Although Sawahashi et al. does disclose repeatedly reading a received signal from memory, (column 6, lines 5-37), it only discloses the use of one code sequence, not a plurality of candidate codes, (Abstract).

Takagi et al., on the other hand, discloses no memory whatsoever for storing a received signal, and only discloses generating sequentially a plurality of candidate codes, not a single code, as claimed herein, (Abstract).

In view of the remarks set forth above, this application is in condition for allowance which action is respectfully requested. However, if for any reason the Examiner should consider this application not to be in condition for allowance, the Examiner is respectfully requested to telephone the undersigned attorney at the number listed below prior to issuing a further Action.

Any fee due with this paper may be charged to Deposit Account No. 50-1290.

Respectfully submitted,


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